

Fig. 1 (Prior Art)

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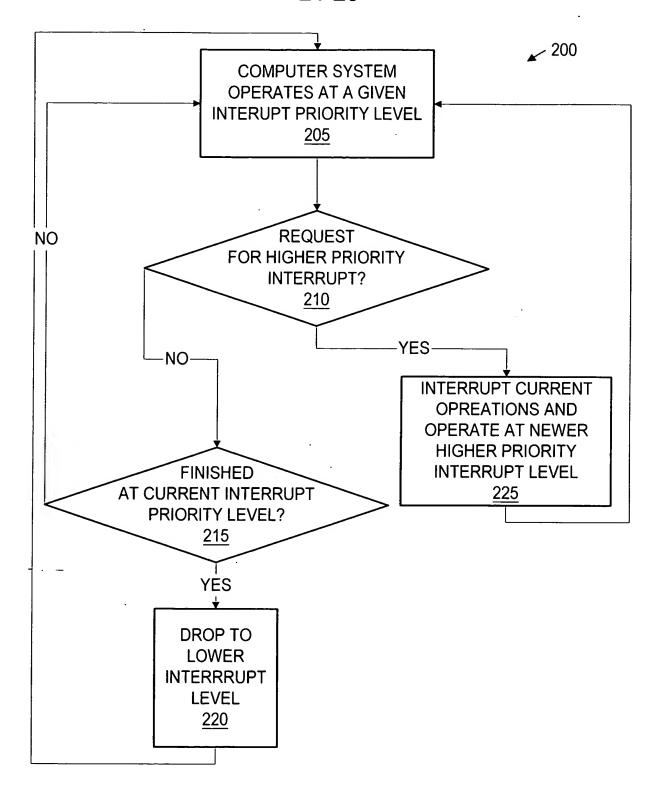


Fig. 2 (Prior Art)



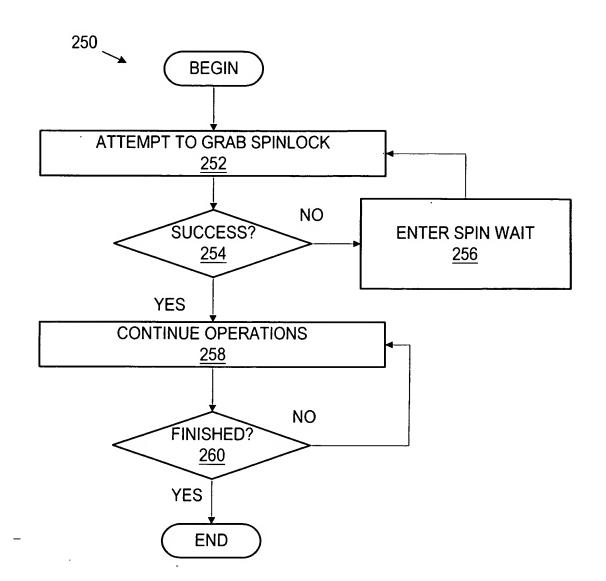


Fig. 3 (Prior Art)

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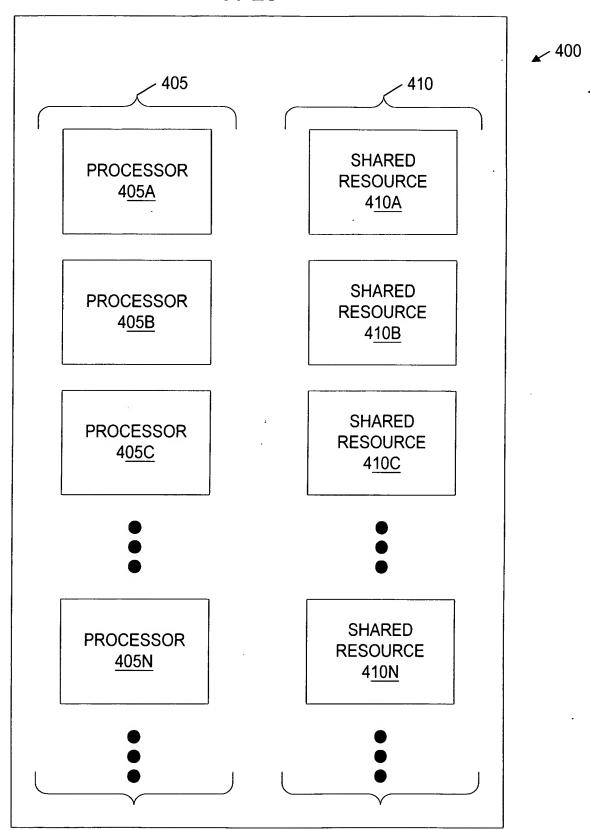
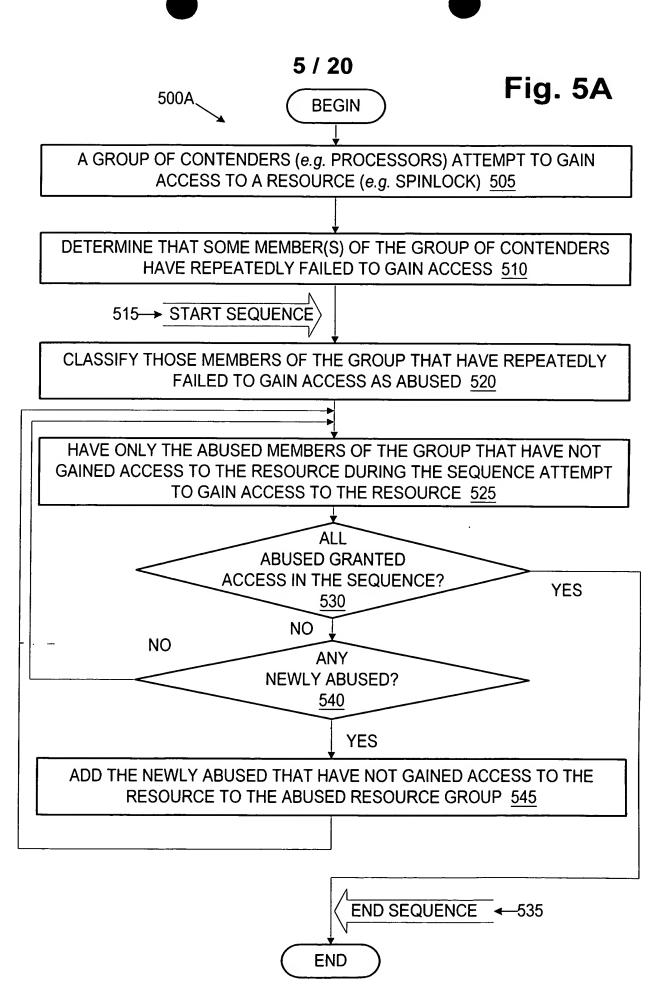
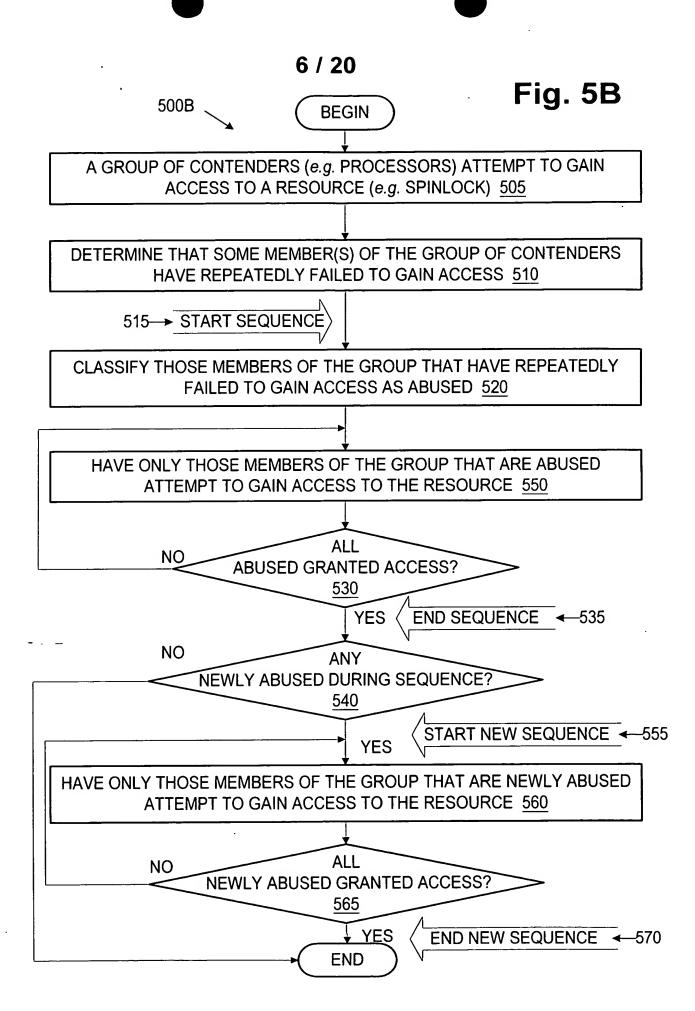


Fig. 4





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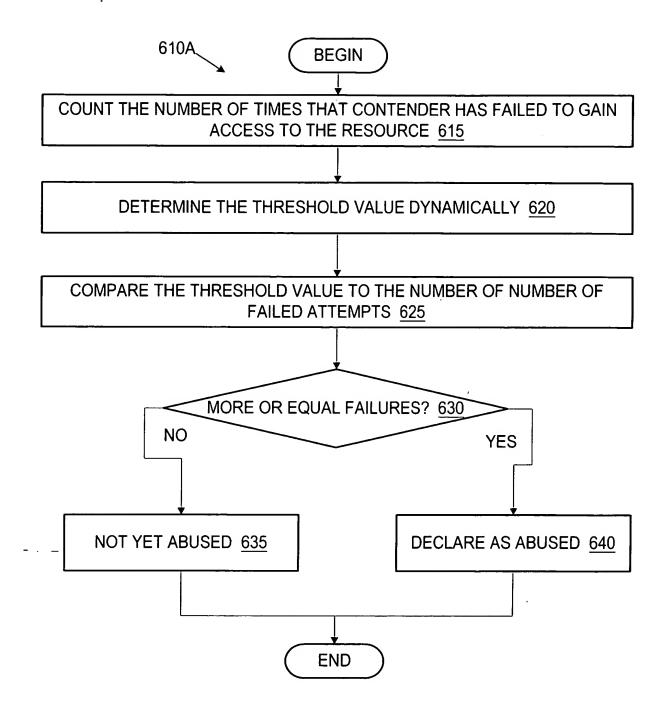


Fig. 6A

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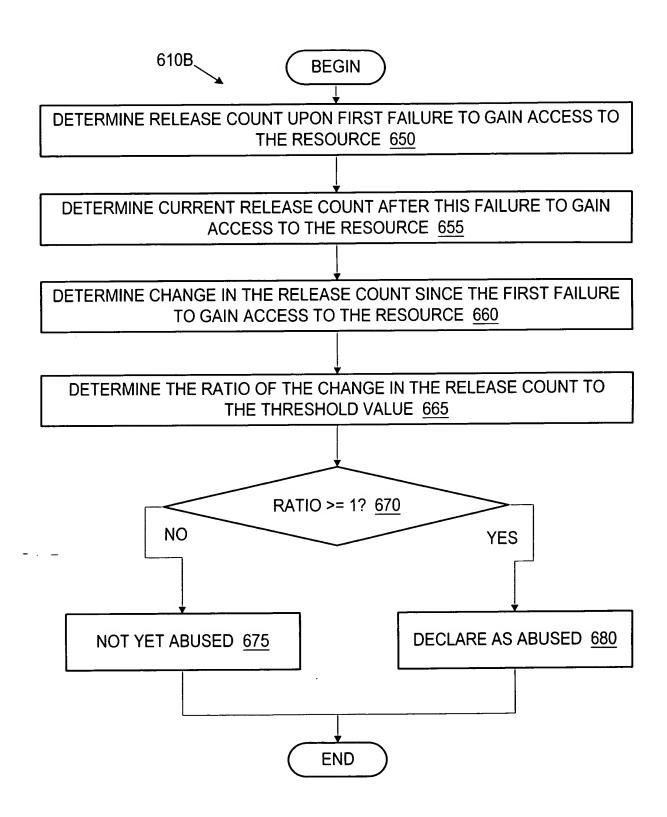
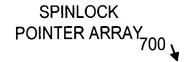


Fig. 6B

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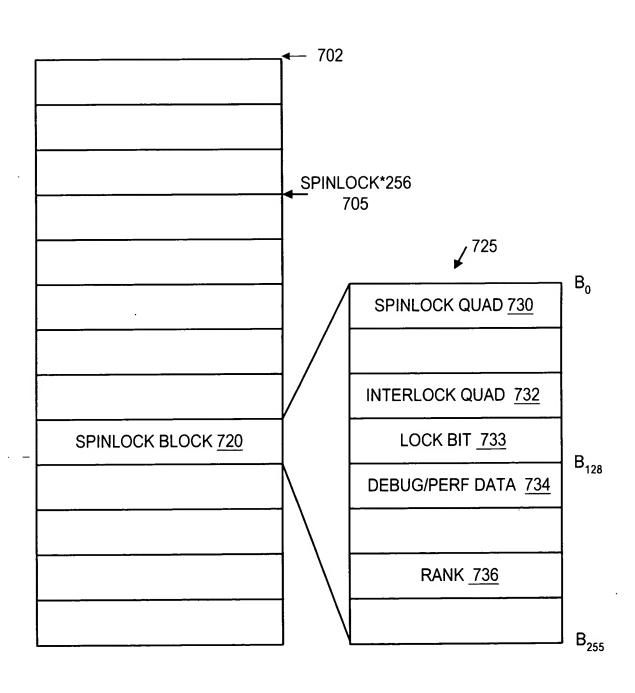


Fig. 7A

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SPINLOCK DATA
BLOCK ARRAY
750

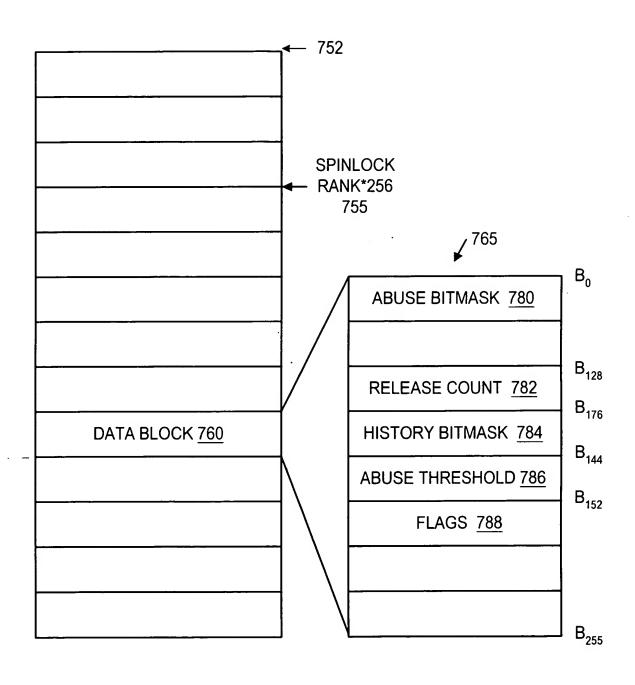
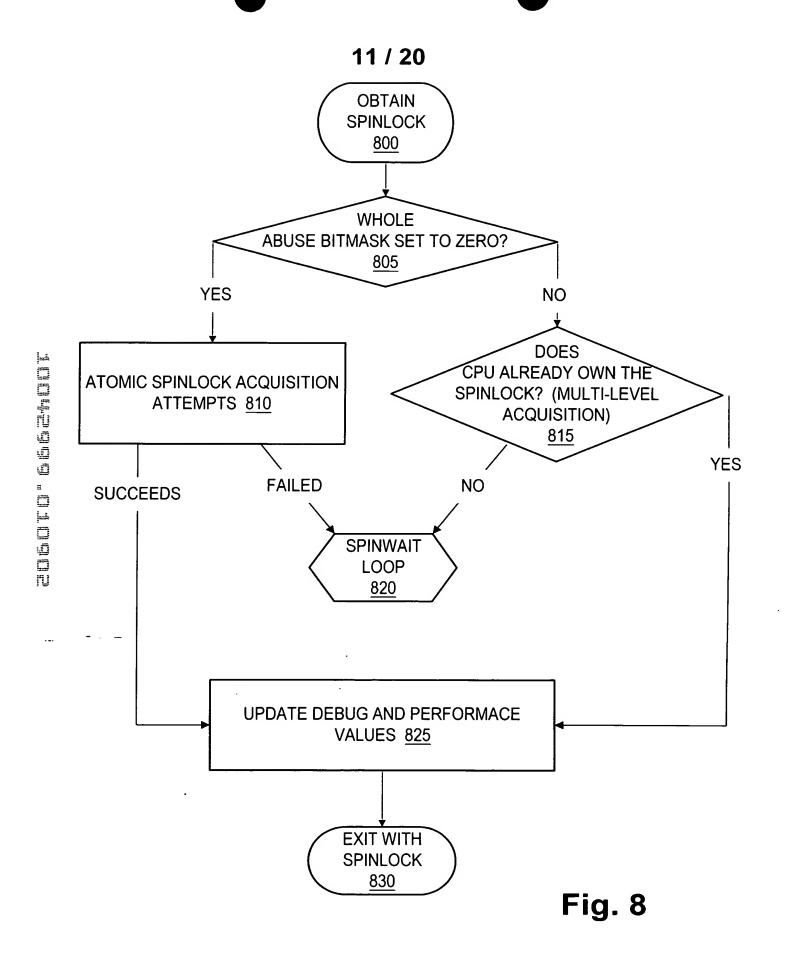


Fig. 7B



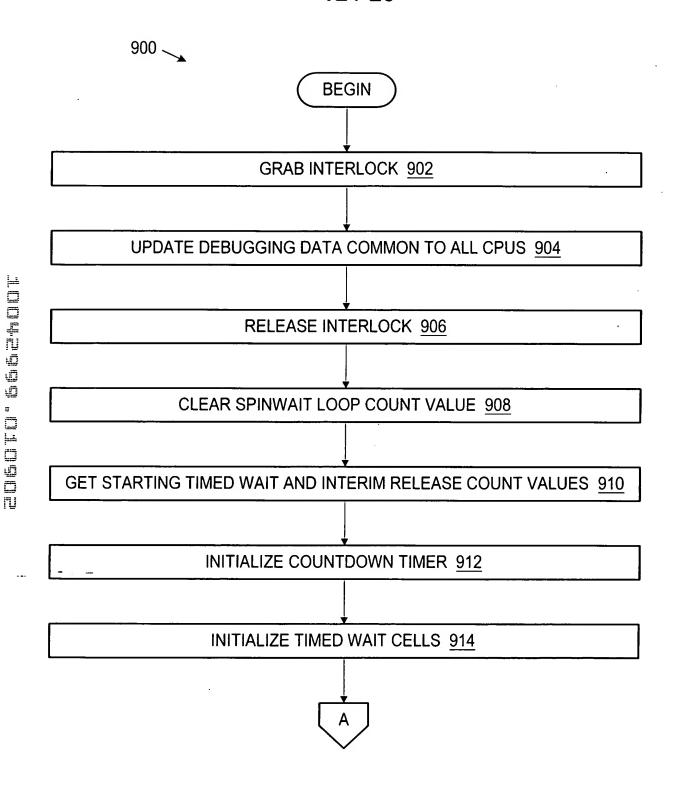
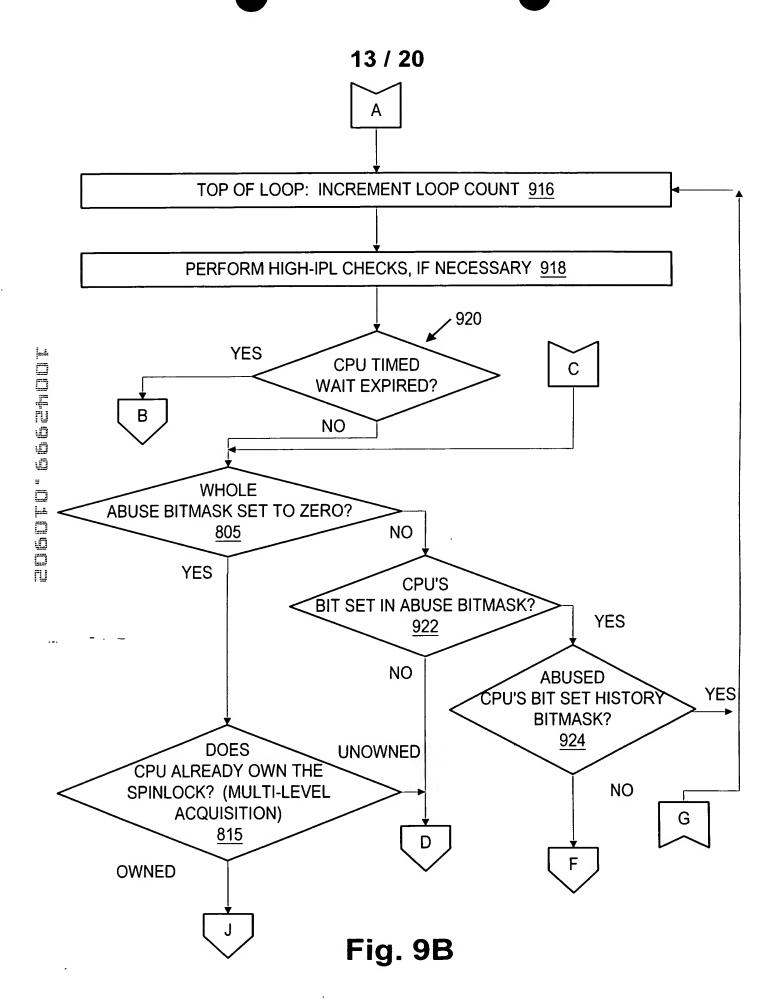


Fig. 9A



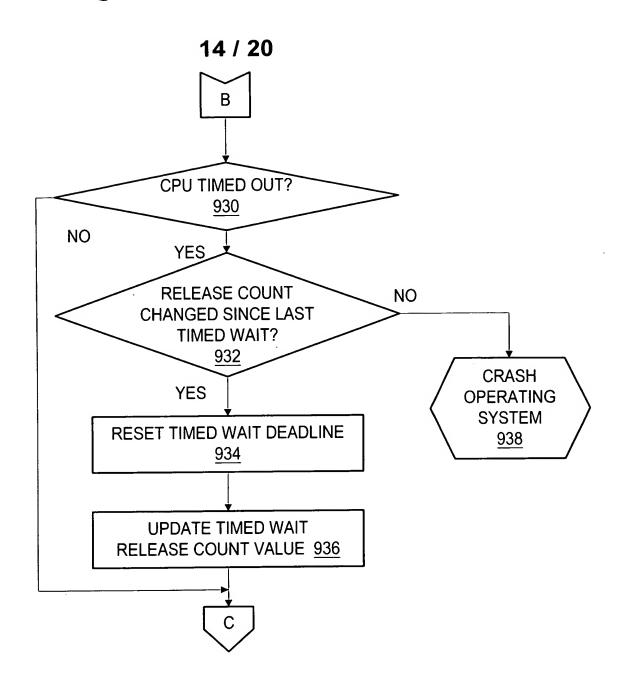


Fig. 9C

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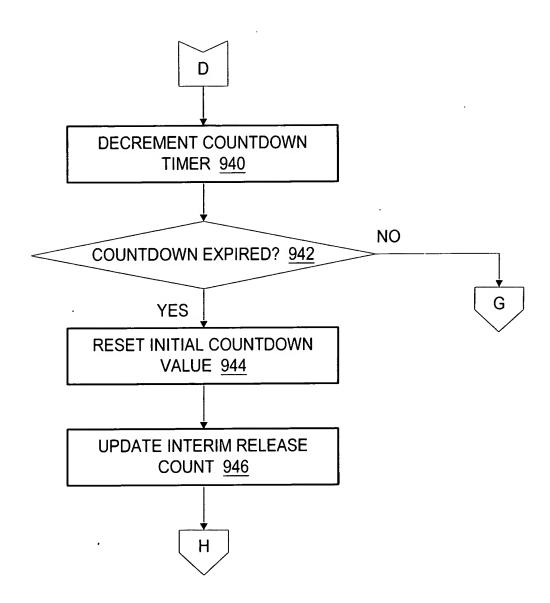


Fig. 9D

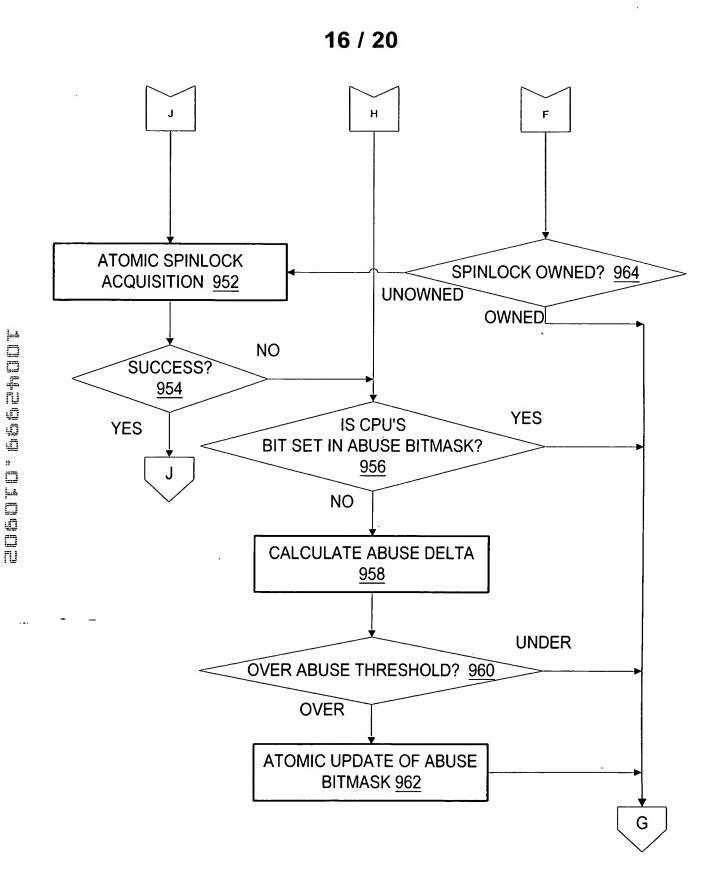


Fig. 9E

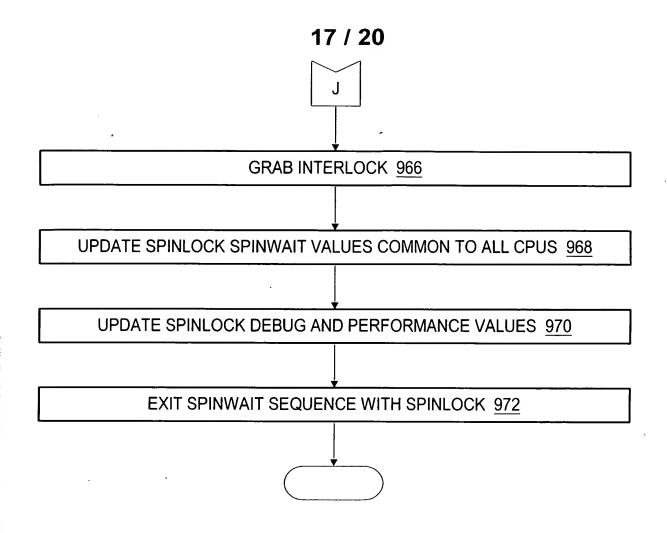


Fig. 9F

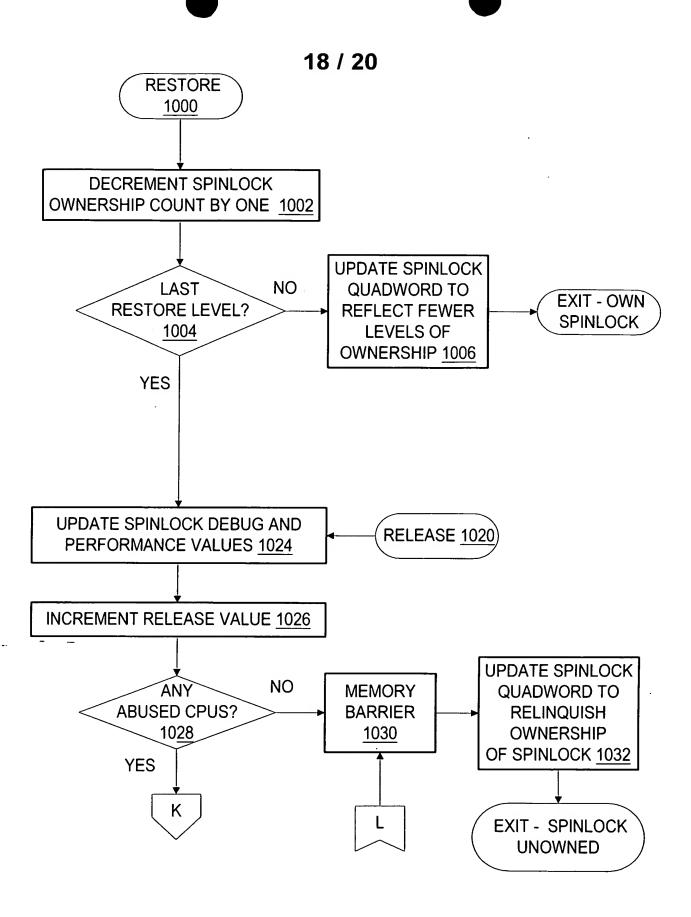


Fig. 10A

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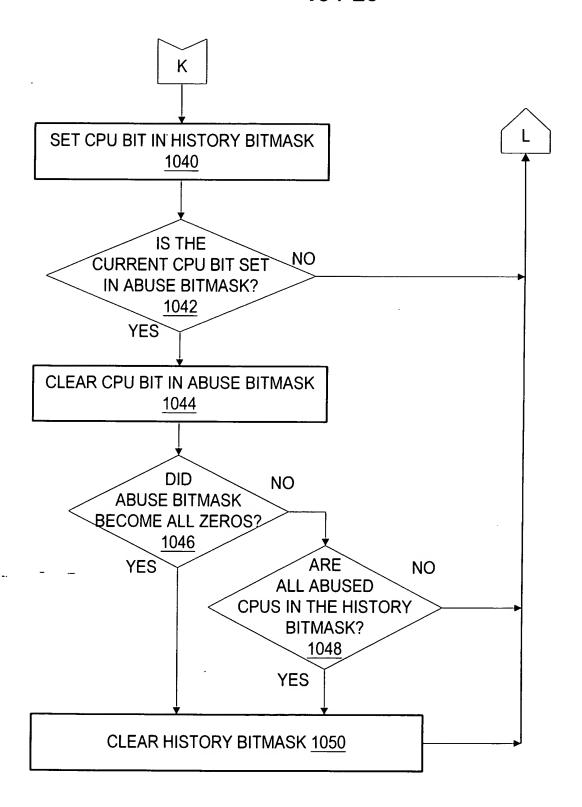


Fig. 10B

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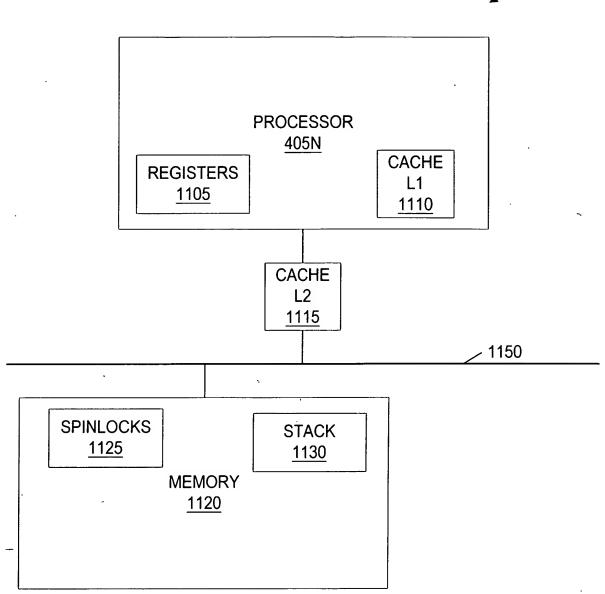


Fig. 11